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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/756,409	01/14/2004	Hirotaka Kawata	118006	2629
25944 75	590 05/23/2005		EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			LE, THAO X	
			ART UNIT	PAPER NUMBER
			2814	
			DATE MAILED: 05/23/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

-		Applicatio	n No	Applicant(s)		
Office Action Summary				KAWATA ET AL.		
		10/756,409 Examiner	9	Art Unit		
		Thao X. Le		2814		
The	MAILING DATE of this communication a					
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1)⊠ Resp	onsive to communication(s) filed on 14	January 2004				
	This action is FINAL . 2b)⊠ This action is non-final.					
3)☐ Since	e this application is in condition for allow	ance except f	or formal matters, pro	secution as to the ments is		
close	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of	Claims					
<u> </u>						
 4) Claim(s) 1-15 is/are pending in the application. 4a) Of the above claim(s) 6,7 and 12-14 is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-5,8-11 and 15 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 						
Application Pa	apers					
,	pecification is objected to by the Examir		· 			
10)⊠ The drawing(s) filed on <u>14 January 2004</u> is/are: a)□ accepted or b)⊠ objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under	35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s)	•					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 01/14/04, 12/29/04. 4) Interview Summary (PTO-413) Paper No(s)/Mail Date. 5) Notice of Informal Patent Application (PTO-152) 6) Other:						

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DETAILED ACTION

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Election/Restrictions

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - Claims 1-5, 8-11 and 15, drawn to a semiconductor device, classified in class 257, subclass 59 and 59.
 - II. Claim6-7 and 12-14, drawn to a method of making a semiconductor device, classified in class 438, subclass 149 and 158.

The inventions are distinct, each from the other because of the following reasons:

- 2. Inventions Group I and Group II are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case that the product as claimed can be made by another and materially different process. For instant, 'the thermally oxidizing the monocrystalline semiconductor layer' can be done by implanting the oxygen ion into the monocrystalline semiconductor layer.
- 3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 4. During a telephone conversation with the Applicant's Attorney, Mr. Michael Britton, on 05/17/05 a provisional election was made WITH traverse to prosecute the

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invention of Group I, claim1-5, 8-11 and 15. Affirmation of this election must be made by applicant in replying to this Office action. Claims 6-7 and 12-14 withdrawn from further consideration by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

5. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Drawings

6. Figure 15-17 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 9. Claims 1-5, 8-11, and 15 are rejected under 35 U.S.C. 102(b) as being anticipated by US 6011275 to Ohtani et al.

Regarding claim 1, Ohtani discloses a transistor in fig. 6B, comprising: at least a monocrystalline semiconductor layer 408, fig. 4C, column 11 lines 40-50 and column 14 line 1-2, and a gate insulating film 511 provided on the monocrystalline semiconductor layer 410, the gate insulating film having a thermal oxide film 511, column 12 line 1, formed on the monocrystalline semiconductor layer 410 and at least one vapordeposited insulating film 515, column 12 line 12, formed on the thermal oxide film 511, fig. 5A-5B.

With respect to monocrystalline semiconductor layer, the interpretation of such layer is a material comprising a single crystal structure. Ohtani discloses layer 408 is crystalline silicon having singular crystal structure, column 6 line 14-20 or column 14 line 1-2. Furthermore, Suzawa (668628) also discloses crystalline silicon is single crystal silicon in column 18 lines 61-63.

Regarding claim 2, Ohtani discloses the transistor according to claim 1, the monocrystalline semiconductor layer 408 being made of monocrystalline silicon, column 10 line 45 and discussion in the above claim 1.

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Regarding claim 3, Ohtani discloses the transistor according to claim 1, the monocrystalline semiconductor layer 408 being a mesa type 410, Fig. 4D.

Regarding claim 4, Ohtani discloses the transistor according to claim 1, the monocrystalline semiconductor layer 408 having a thickness of 35 nm, column 12 line 6.

Regarding claim 5, Ohtani discloses the transistor according to claim 1, the thermal oxide film 511 of the gate insulating film having a thickness of 30 nm, column 11 line 67.

Regarding claims 8-11 and 15, Ohtani discloses an electro-optical device, comprising: a transistor, fig. 6B, wherein a transistor according to claim 1 being provided as a switching element in a display area, column 20 line 65, fig. 14A, a electro-optical device, column 21 line 22, a semiconductor device, fig. 14A-14F.

In the recitation 'an electro-optical device' and 'an electronic apparatus' has not been given patentable weight because it have been held that a preamble is denied the effect of a limitation where the claim is drawn to a structure and the portion of the claim following the preamble is a self-contained description of the structure not depending for completeness upon the introductory clause. Kropa v. Robie, 88 USPQ 478 (CCPA 1951)

Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thao X. Le whose telephone number is (571) 272-1708. The examiner can normally be reached on M-F from 8:00 AM - 4:30 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael M. Fahmy can be reached on (571) 272 -1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thao X. Le Patent Examiner 18 May 2005